Atty. Dkt. No. 1001-0186 1st Inventor: Frank P. Helms Attorney: Mark Zagorin, (512) 347-9030

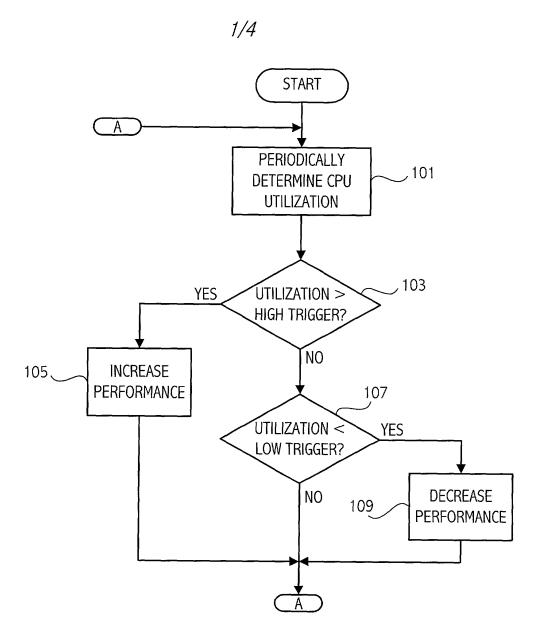


FIG. 1

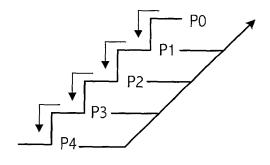


FIG. 2

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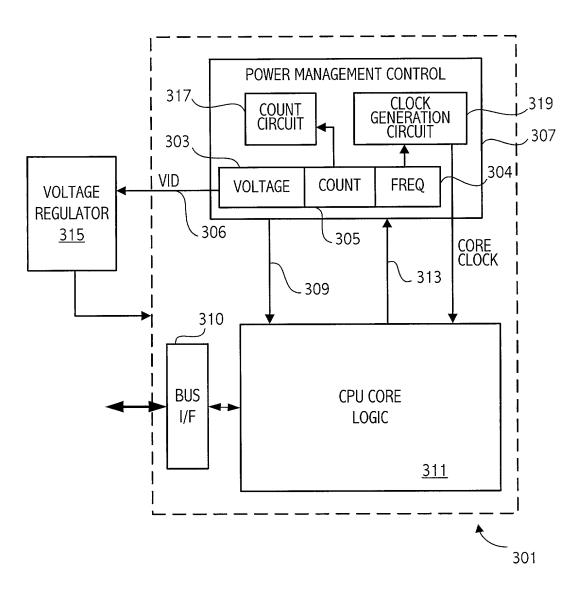


FIG. 3

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## *3/4* **FIG. 4**

Temperature	Type of Trip Point	Action	Available Performance States	Temp Range
103C	Critical Trip Point	OS performs orderly shut down	P2, P3, P4	>80C
980	Passive Trip Point (max active cooling)	OS throttles processor (50%)		
≥80C	Maximum Pstate Change	Transition to Pstate2 as max		
≤78C	Maximum Pstate Change	Transition to Pstate1 as max	P1, P2, P3, P4	70-80C
≥700	Maximum Pstate Change	Transition to Pstate1 as max		
≤68C	Maximum Pstate Change	Transition to Pstate 0 as max	P0, P1, P2, P3, P4	<70C
65C	Active trip point 2 (max active cooling)	Turn on Fan 100%		
60C	Active trip point 3 (med active cooling)	Turn on Fan 66%		
50C	Active trip point 4 (min active cooling)	Turn on Fan 33%		

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